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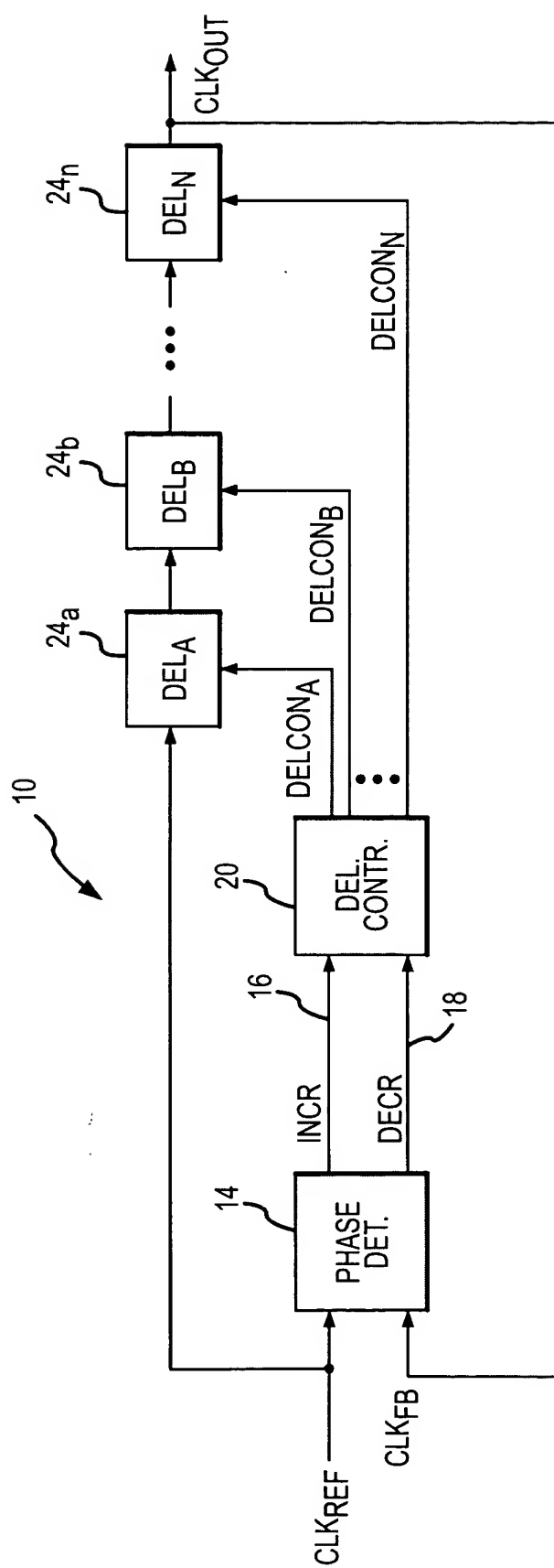


FIG.1

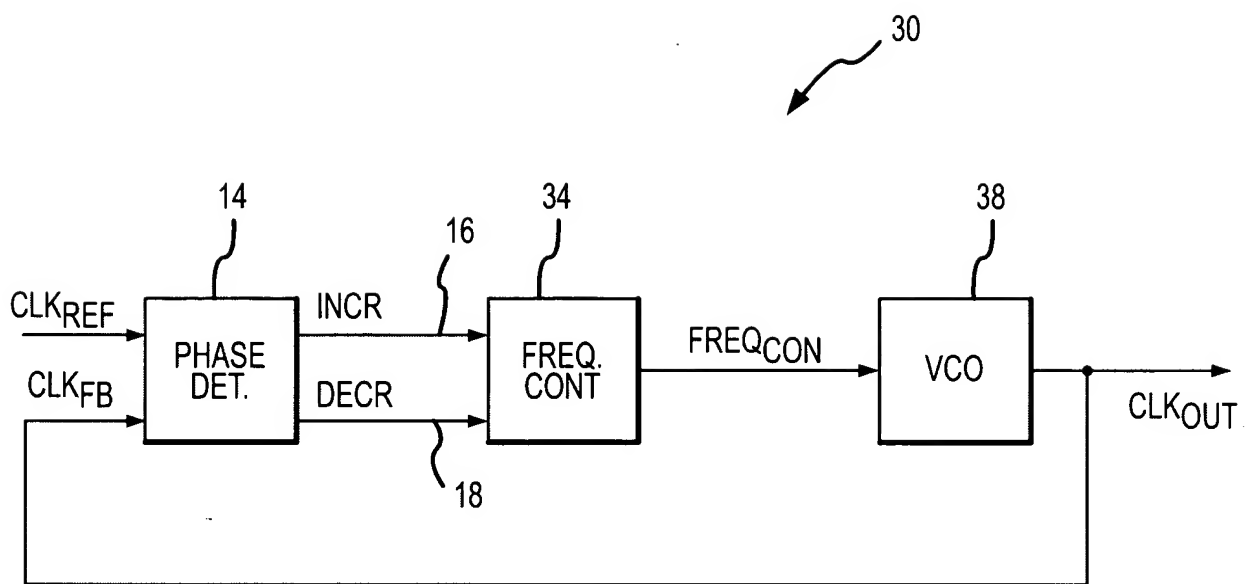
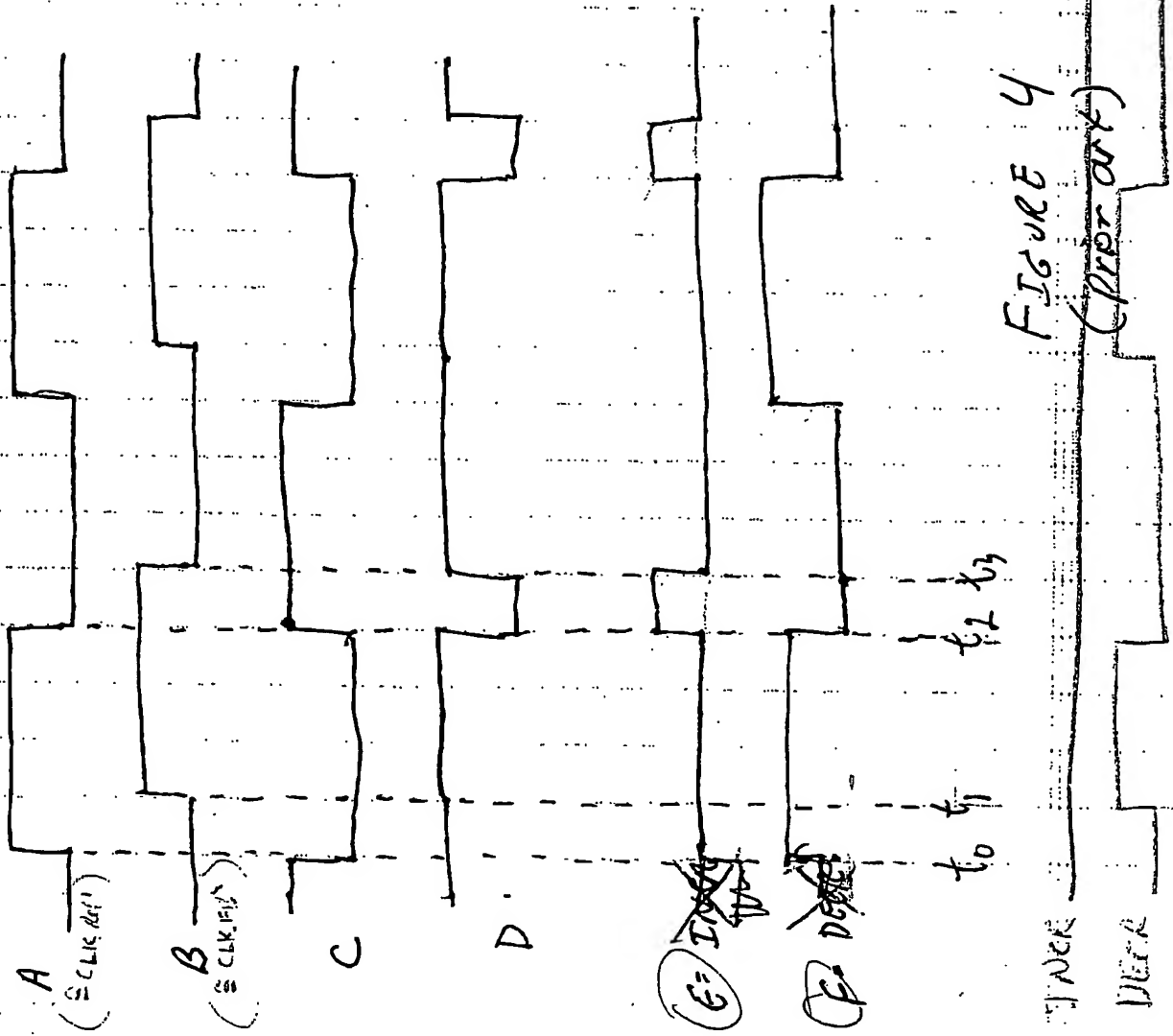


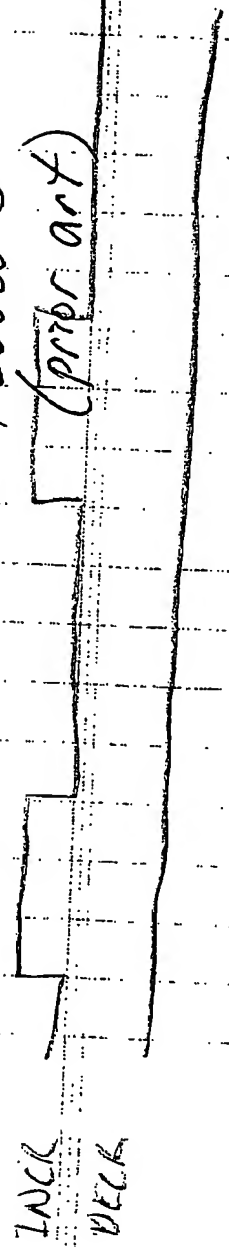
FIG.2



A



FIGURE 5
(prior art)



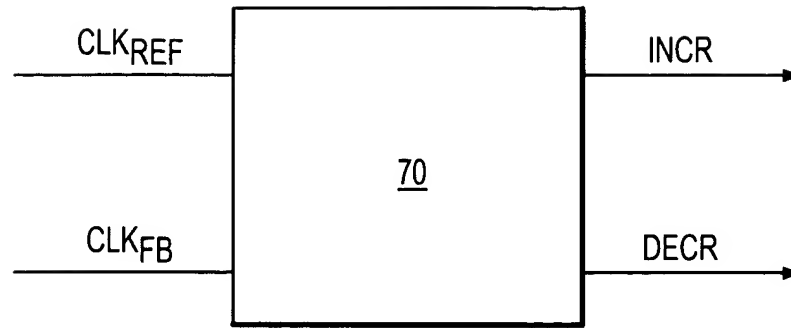


FIG.6

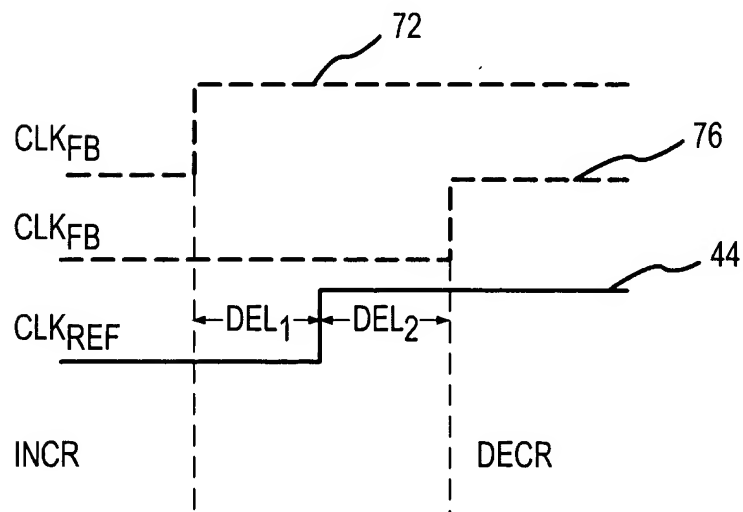
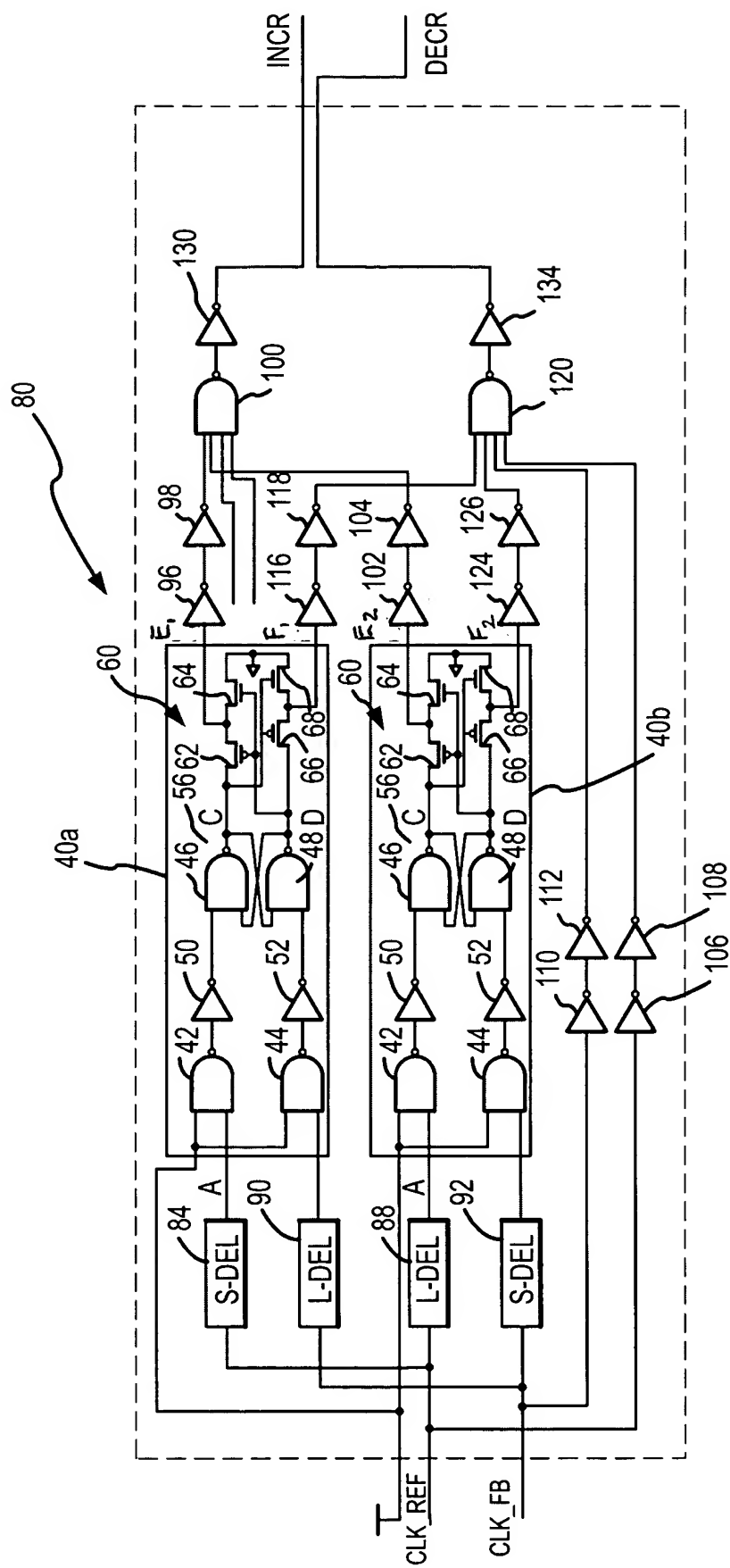


FIG.7



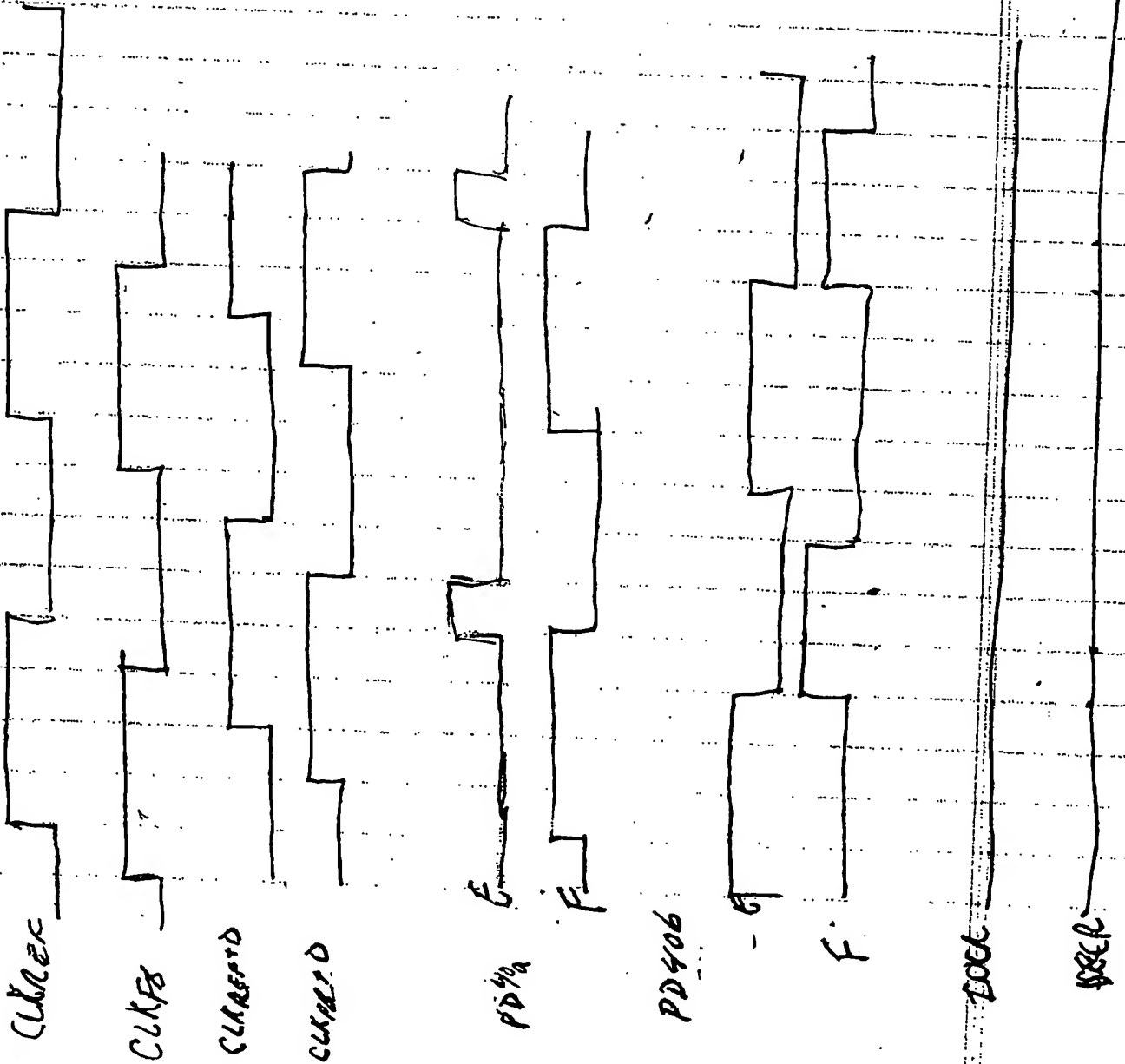


FIGURE 9

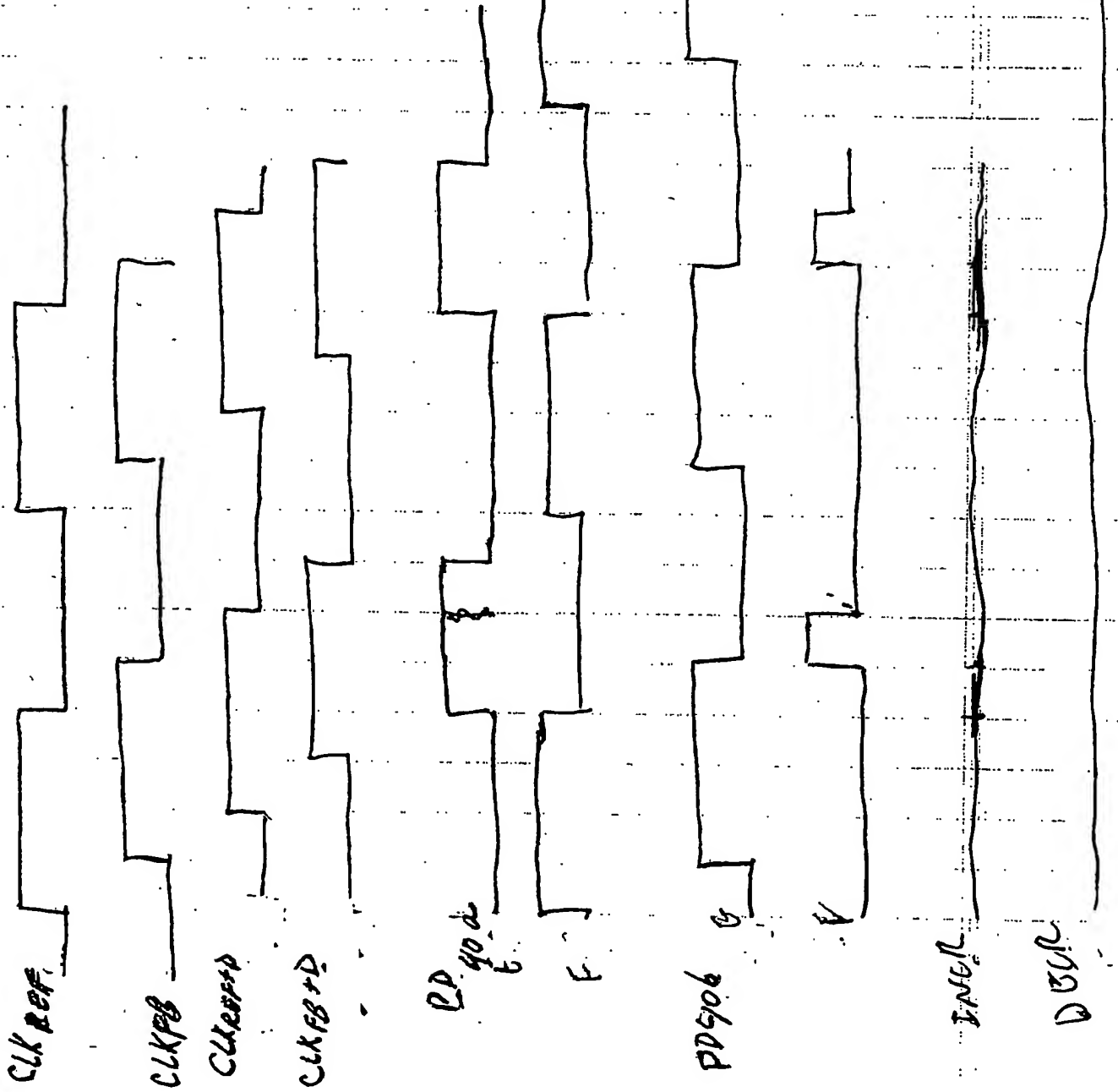


FIGURE 10

CLK REF - CLK FB
SIGNAL SUPPLY

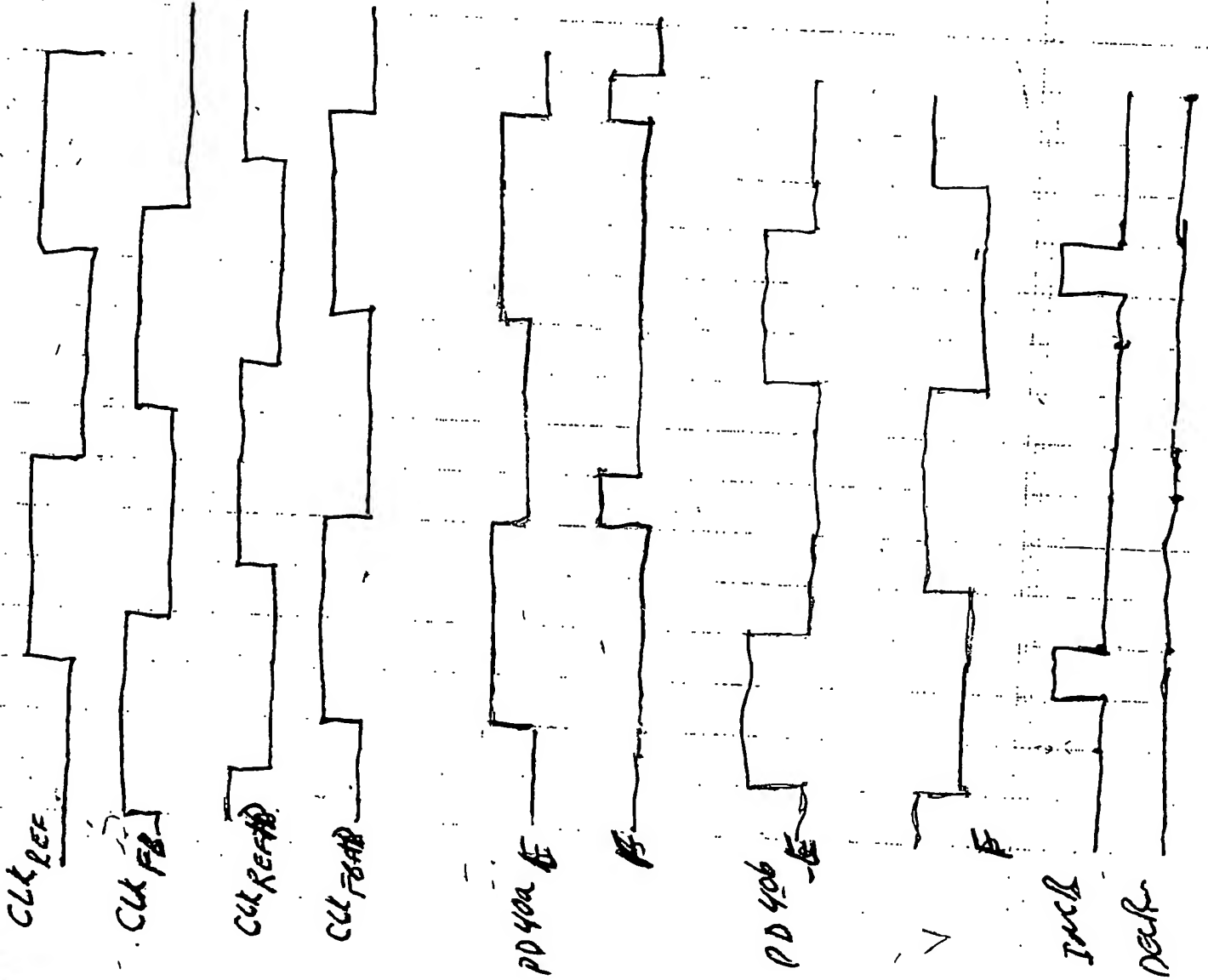


Figure 11

$$CLK_{REF} - CLK_{FB} > (t_{DEL} - t_{DEL})$$

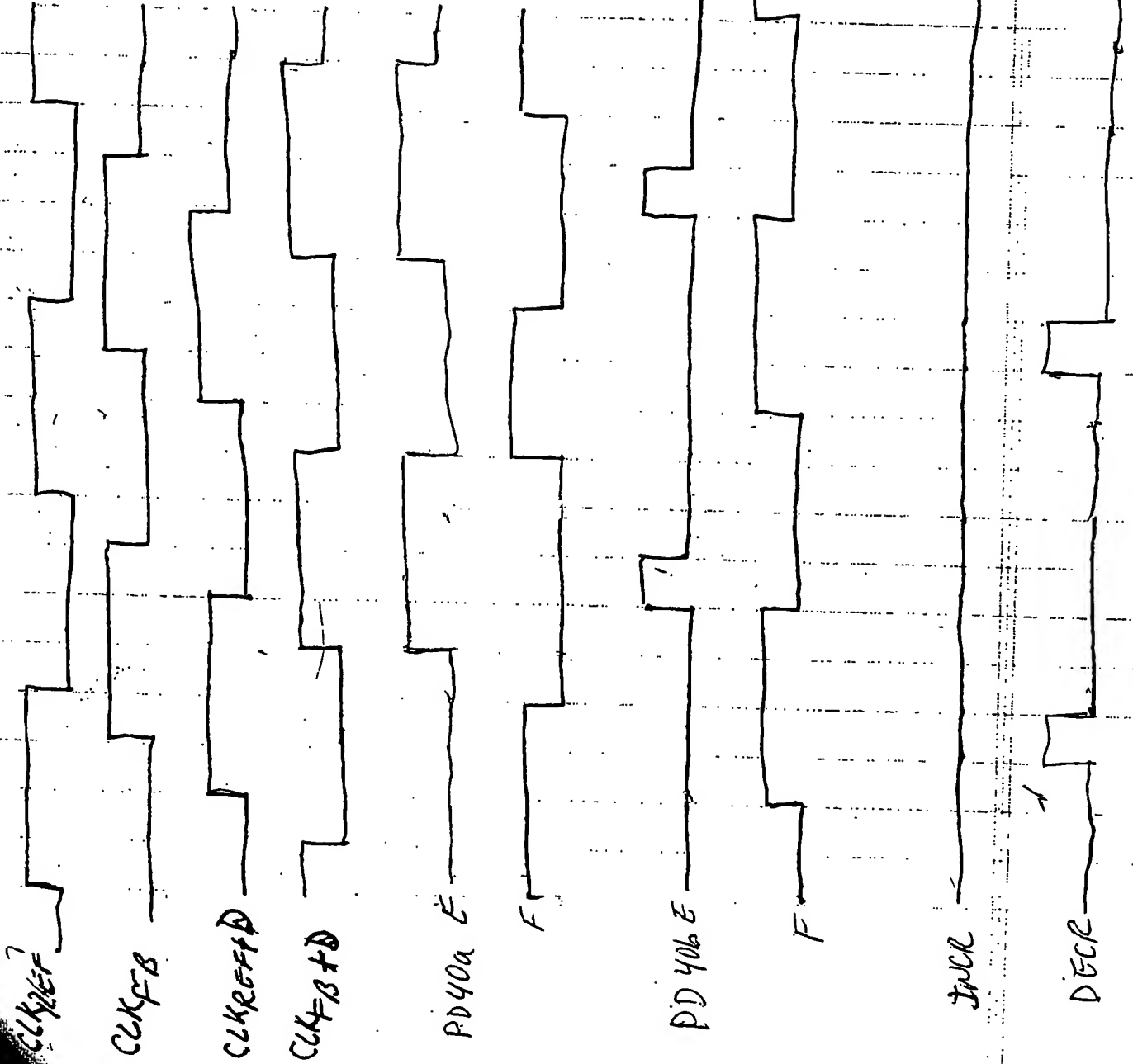


FIGURE 12

$$|CLK_{REF} - CLK_{FB}| > (t_{DEL} \cdot F_{SPEL})$$

★

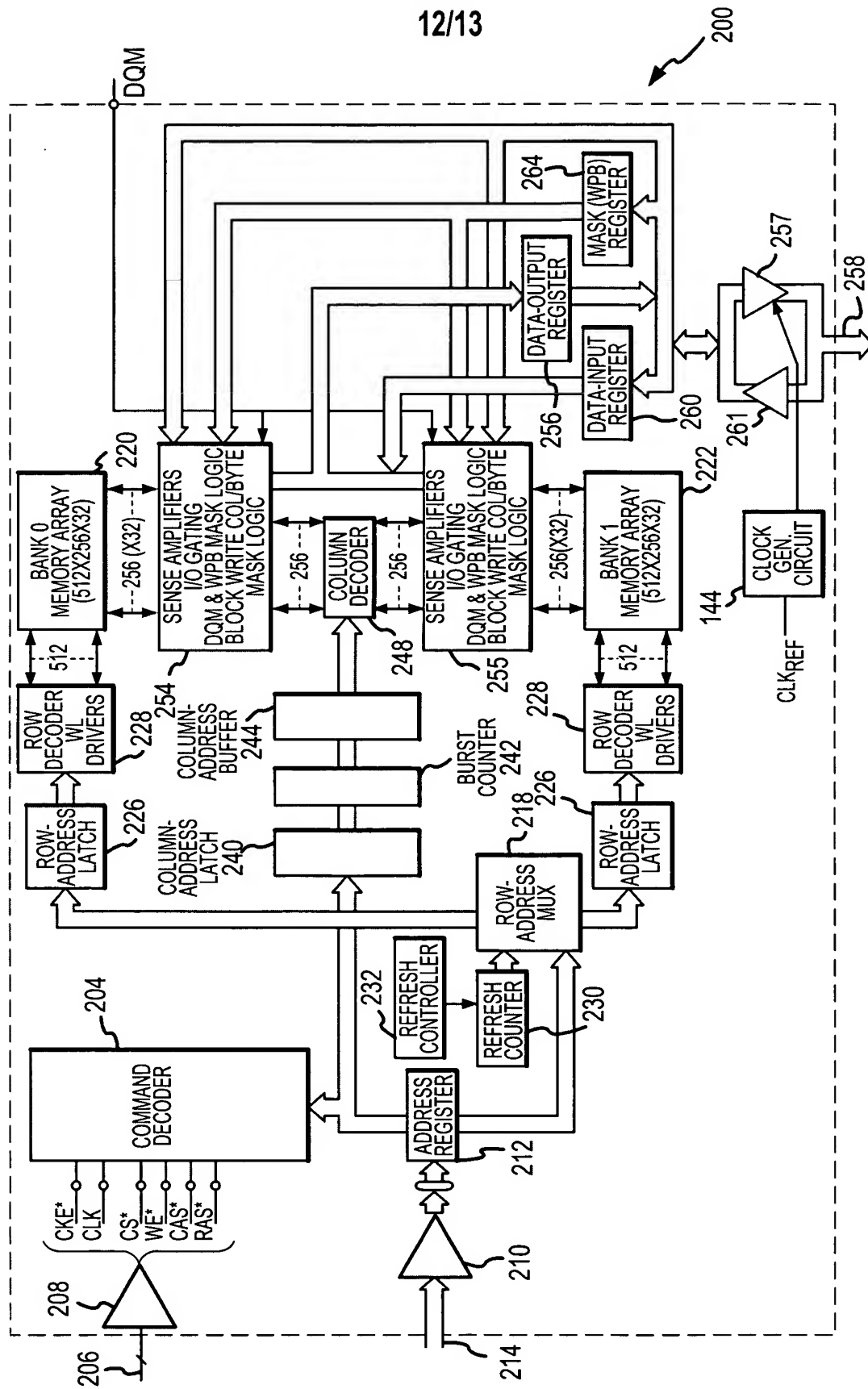


FIG.13

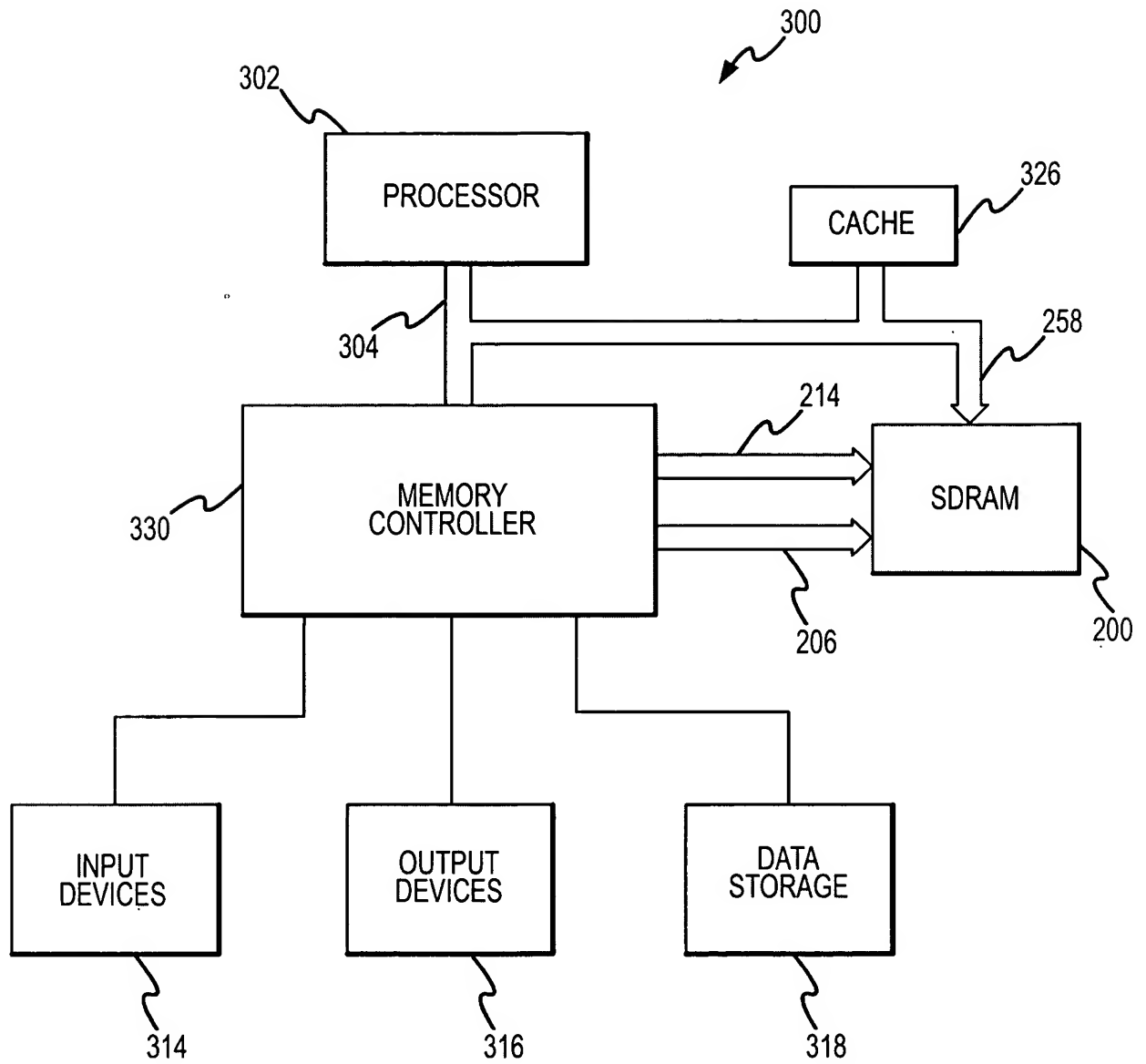


FIG.14